

Disclaimers: These products are not recommended for any life critical applications. Specifications quoted below are typical values provided as a guidance for selection and for comparative operations. For critical operations users are advised to characterize the product under actual operating conditions of specific customer applications.

Parameter	Conditions	Value	Unit
Jitter	F = 622 Mhz	28.5	ps
Jitter	F = 60.0 Mhz	24.9	ps
Phase noise	Foffset from carrier=1khz	-95	dBc
Phase noise	Foffset from carrier=100khz	-108	dBc
Phase noise	Foffset from carrier=1 Mhz	-127	dBc
Output format	LVDS		
Enable /Disable	Optional, please specify		
Ambient temperature		-40 to 85	Deg C
Fredquency resolution availability	Customer can use to specify required frequency	6 decimal places	
Frequency range available		750 Khz to 1.35 Ghz	
Supply voltage		3.3 ± 5%	
Input supply current	100 Ohm load	100 (typ)	mA
Output loading		100 (typ)	Ohms
Start up time		100 (typ)	ns
Enable/ Disable time	(Option) Please order	100 (typ)	ns
Differential output voltage		0.6 (typ)	V
Output offset voltage		1.3 (typ)	V
Output symmetry	@ 50% peak to peak Voltage	45% - 55%	%
Output enable voltage	(Optional)	> 70% of VDD	
Output disable voltage	(Optional)	<30% of VDD	
Output rise time	20% to 80%	550 (typ)	ns
Output fall time	80% to 20%	550 (typ)	ns

Signal Processing Group Inc., 51W Elliot Road, #103, Tempe, Arizona 85284. Signal Processing Group Inc., designs, develops and manufactures, analog and RF/wireless ASICS and modules using state of the art semiconductor, PCB and assembly technology. Please contact us at spg@signalpro.biz.